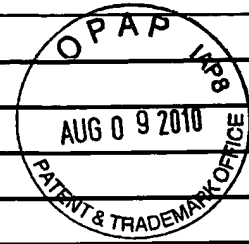


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		Application Number	10/538,563
		Filing Date	June 15, 2005
		First Named Inventor	Edwin Rijpkema
		Art Unit	2473
		Examiner Name	Kao, Jutai
Attorney Docket Number	EIN-NL021330 (STNX01-21330)		
Sheet	2	of	2



NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Edwin Rijpkema, et al., "A Router Architecture for Networks on Silicon", Proceedings of Progress 2001, 2nd Workshop on Embedded Systems, pg. 1-8.	
		Mamoru Takajyo, et al., "Improvement for Packet Priority of the Packet Switch with Multiple Input Queues for Every Output Port", The Institute of Electronics, Information and Communication Engineers, Technical Report of IEICE SSE97-13, April 1997, 8 pages.	
		Translation of Office Action dated May 22, 2009 in connection with Japanese Patent Application No. 2004-561738.	

Examiner Signature	Date Considered
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